Monolithic On-chip Magneto-optical Isolator with 3 dB Insertion Loss and 40 dB Isolation Ratio

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ABSTRACT: On-chip optical isolators constitute an essential building block for photonic integrated circuits (PICs). Here, we experimentally demonstrated a magneto-optical isolator monolithically integrated on silicon featuring 3 dB insertion loss and 40 dB isolation ratio, both of which represent significant improvements over state-of-the-art. The isolator is also fully passive and operates under a simple unidirectional magnetization scheme. Such superior performance is enabled through a three-way combination of a strip-loaded waveguide design, a compositionally optimized chalcogenide glass as the light guiding medium, and low-loss taper structures created via grayscale-lithographic processing. The device represents an important step toward a practical solution for on-chip isolation in PICs.

KEYWORDS: Isolators, magneto-optical materials, integrated optics devices, resonators, chalcogenide glass, grayscale-lithography
It is worth pointing out that the quality of deposited polycrystalline garnet films is not the main performance-limiting factor in these early prototypes. For instance, Faraday rotation (FR) up to 2650 deg/cm (measured at saturation magnetization) has been obtained in deposited ferrimagnetic Ce:YIG films,\textsuperscript{36} approaching values measured in single-crystalline films of similar compositions.\textsuperscript{37,38} Admittedly, optical loss of the deposited Ce:YIG films (40 dB/cm at 1550 nm\textsuperscript{39}) is still considerably higher than that of their single-crystalline counterpart (<10 dB/cm\textsuperscript{37}). The material figure-of-merit (material FOM, defined as the ratio between FR and material loss) reaches 66 deg/dB even with such relatively high material losses, which predicts an IL down to 0.8 dB for a Faraday rotator if parasitic losses (other than material attenuation) are absent; far superior compared to the experimentally obtained performance. Understanding the nonidealities that account for such subpar device characteristics is thus critical to further improving on-chip isolator performances.

In this Article, we report the experimental demonstration of a monolithic on-chip isolator exhibiting an IL as low as 3.0 dB and an IR exceeding 40 dB, both of which represent major improvements over state-of-the-art. Such unprecedented performance builds on our understanding of the optical loss mechanisms in prior isolator devices and implementation of corresponding loss mitigation strategies, which we present in the next section. A novel isolator architecture based on strip-loaded waveguide structures with adiabatic vertical tapers was applied to eliminate the parasitic losses. Design, fabrication, and characterization of the device are elaborated in the succeeding sections.

Optical Loss Mechanism Analysis. Figure 1b schematically illustrates the layout of a resonator-based magneto-optical isolator. When a magnetic field is applied, degeneracy between the clockwise and counterclockwise propagating modes of the resonator is lifted due to nonreciprocal phase shift (NRPS) in the magneto-optically active waveguide, leading to asymmetric transmission in the bus waveguide for forward and backward propagating light. The sign of NRPS depends on the direction of magnetization with respect to the light propagation direction. Therefore, unidirectional magnetization of an entire closed-loop resonator leads to vanishing resonance splitting since NRPS on opposite sides of the resonator has different signs and cancels out. This issue can be resolved either by applying a radial magnetic field to each resonator, or keeping only part of the resonator magneto-optically active. Radial magnetization demands a needle-shaped magnet\textsuperscript{17} or an electromagnetic microcoil\textsuperscript{18} to be placed in close proximity to each and every resonator, considerably increasing device complexity.

The approach of maintaining magneto-optical activity in only part of the resonator was previously implemented by opening a window in the resonator top cladding prior to garnet film deposition such that the magneto-optical film only contacts the waveguide core in the window region. Figure 1c shows a cross-sectional scanning electron microscopy (SEM) image of a silicon-on-insulator (SOI) waveguide covered with deposited magneto-optical oxides (ref 6). In this prototypical device, the deposited film consists of a bottom unsubstituted yttrium iron garnet (YIG) buffer layer and a Ce:YIG layer on top, the latter of which is the main magneto-optically active medium. The unsubstituted YIG buffer layer,
which readily crystallizes into the garnet phase and subsequently serves as a template promoting Ce:YIG crystallization, is essential to obtaining a high-quality Ce:YIG film. Our prior work has revealed that when thickness of the YIG buffer layer is reduced to below a certain threshold value, crystallization of the top Ce:YIG layer into the magneto-optically active garnet phase is hindered.46

The device configuration, however, incurs two undesired sources of parasitic loss. First of all, we found that it was more difficult to fully crystallize garnet films deposited on substrates with sidewall topography (e.g., waveguides) and observed greater formation of secondary phases such as iron oxide with unacceptably high optical absorption and decreased magneto-optical activity. This may be a consequence of the reduced total film thickness (and hence YIG buffer layer thickness) on the sidewalls compared to the film on top of the waveguide, as is evident from Figure 1c. This issue cannot be resolved by simply increasing the overall film thickness, as the stack thickness is bound by CTE mismatch to no more than a few hundred nanometers.41 Furthermore, the windows opened in the waveguide cladding for garnet deposition introduce junctions with abrupt index changes, causing significant scattering losses.

Figure 1d illustrates the different loss contributions in the isolator device in ref 6. Details of the loss estimates are compiled in the Supporting Information. Clearly, parasitic absorption resulting from secondary phases and scattering loss at the junctions are the major contributors accounting for 80% of the total loss. In the next section, we focus on an isolator architecture designed to eliminate these parasitic losses.

Isolator Design and Fabrication. The new isolator design is depicted in Figure 2a (perspective view) and 2b (cross-section). The basic building block of the isolator is a nonreciprocal magneto-optical resonator comprising strip-loaded waveguides on a deposited Ce:YIG film. The film is deposited on a planar silica-on-silicon layer and is fully crystallized into the garnet phase (confirmed by X-ray diffraction and vibrating sample magnetometry measurements) with low optical loss. By sandwiching a vertically tapered oxide spacer layer between the strip-loaded waveguide core and the Ce:YIG film, only a fraction of the microring is in direct contact with the Ce:YIG layer and magneto-optically active. The oxide taper creates an adiabatic mode transformer, which minimizes scattering and Fresnel reflection losses between waveguide sections with and without the spacer layer. The design therefore eliminates the two dominant sources of parasitic optical losses, i.e., absorption from secondary phases and waveguide junction scattering, which underpins the superior isolation performance we experimentally obtained in the device.

The design also features an added benefit through enhancing nonreciprocity in the waveguide. The unsubstituted YIG seed layer exhibits Faraday rotation with a sign opposite to that of Ce:YIG,36 which partially cancels out the waveguide NRPS. The deleterious effect is aggravated in the traditional waveguide structure as the YIG layer sits directly on top of the Si core, thereby having large spatial overlap with the evanescent field. In the strip-loaded waveguide configuration, the effect is alleviated since the Ce:YIG layer rather than the YIG layer is in direct contact with the waveguide core. Our numerical modeling suggests that the configuration contributes to a 26% enhancement in NRPS of the waveguide.

Figure 2c plots the simulated NRPS and FOM of the strip-loaded waveguide on Ce:YIG as functions of the refractive index of the core material. Here, the waveguide FOM (a dimensionless quantity) is defined as

\[
FOM_{WG} = \frac{\Delta \beta}{\alpha}
\]

where \(\Delta \beta\) denotes the waveguide NRPS for the quasi-TM mode, i.e., the propagation constant difference of forward and backward propagating modes in the waveguide (in radian per cm), and \(\alpha\) gives the waveguide propagation loss (in cm\(^{-1}\)). The waveguide FOM scales with the material FOM defined in

Figure 2. (a) Tilted-view and (b) cross-sectional schematic of the new isolator design; (c) nonreciprocal phase shift and waveguide figure of merit computed as functions of the strip-loaded waveguide core refractive index: the insets are modal profiles of strip-loaded waveguides when the core material is chosen as silicon nitride \((n = 2.0)\), GeSbSe glass \((n = 2.7)\), and amorphous silicon \((n = 3.6)\), where the scale bars correspond to 500 nm; (d) fabrication process flow for the isolator device.
the previous section but additionally depends on the waveguide geometry, and ultimately dictates the performance of magneto-optical isolators. In Figure 2c, the waveguide dimensions are optimized to achieve maximum waveguide FOM for each core material index value while ensuring single-mode operation. The corresponding waveguide design parameters are tabulated in the Supporting Information. The simulations show that, while NRPS monotonically rises with increasing refractive index of the core over the range of indices considered, modal confinement in the Ce:YIG layer and hence optical loss also grows as a result of the reduced core thickness and increasing field discontinuity at the boundaries. The trade-off therefore points to an optimal core index of 2.6 to reach the maximum waveguide FOM.

Based on this insight, we selected Ge_{22}Sb_{18}Se_{60} (GeSbSe), a chalcogenide glass (ChG), as the waveguide core material. The glass has a refractive index of 2.7 near 1550 nm wavelength, close to the optimal value of 2.6. The chemical stability and optical transparency of GeSbSe glasses have also been experimentally established. Moreover, we have already matured fabrication protocols for low-loss ChG photonic devices including on-chip resonators with quality factors (Q-factors) exceeding one million and applied the technique to realize a wide array of functional photonic components and systems. The low deposition and processing temperatures of ChG further mitigates the risk of film cracking or delamination due to thermal stress accumulation. We note that the isolator architecture is, however, generic and can also make use of other low-loss deposited dielectric materials such as silicon nitride and amorphous silicon as the waveguide core. Additionally, the strip-loaded waveguide layout as well as the low-loss vertical taper structure are equally applicable to enhancing the performance of other isolator device platforms such as those based on Mach–Zehnder interferometers (MZIs). The process flow to fabricate the isolator structure is shown in Figure 2d.

Figure 3a shows a top-view optical microscopy image of the fabricated resonator isolator, where the color fringes on the substrate manifest the gradual thickness variation of the oxide spacer layer. Figure 3b displays a cross-sectional SEM image of the ChG waveguide core sitting on the spacer layer in a region of device without the tapered spacer. The waveguide has nearly vertical sidewalls and a well-defined width of 650 nm, an optimized value following our numerical simulation results.

**Isolator Characterization.** Figure 4a illustrates a schematic diagram of the isolator characterization setup. The fabricated device was tested on a Newport Autoalign station where light was coupled in and out of the waveguides via end fire coupling through tapered fibers (Nanonics Imaging Ltd.) mounted on computerized motion stages. The device chip was covered with an index matching fluid (Cargille-Sacher Laboratories Inc.), which helps to minimize Fresnel reflection in fiber-to-chip coupling. An optical vector analyzer (OVA, Luna Innovations Inc.) with built-in external cavity tunable laser was used in conjunction with an erbium-doped fiber amplifier (Amonics Ltd.) as the interrogation light source. The waveguide output spectrum was also monitored by the OVA. During the test, a rare-earth permanent magnet was placed near one end of the device chip to impose a nearly unidirectional magnetic field of approximately 0.1 T on the devices, sufficient to saturate the magnetization of the ferrimagnetic Ce:YIG film. The isolation performance was validated by reversing the light propagation direction. The measurement was repeated five consecutive times and averaged to suppress unwanted resonant peak drift due to temperature fluctuations.

The bidirectional transmission spectra of quasi-TM mode in the isolator device are presented in Figure 4b. As shown in the figure, the device exhibits an IL as low as 3.0 dB and a high IR of 40 dB, both of which set the performance records for monolithic magneto-optical isolators. The spectra, averaged over five consecutive measurements, reveal a nonreciprocal resonant peak shift of (44 ± 4) pm, in agreement with our simulation results. The resonant peak positions at both forward and backward directions recorded during the five repeated measurements are plotted in Figure 4c. Wavelength dependence of the nonreciprocal resonance shift was characterized in the wavelengths range of 1540 to 1590 nm. The measurement results, plotted in Figure 4d alongside theoretical simulations, indicate a nearly wavelength-independent nonreciprocal resonance shift resulting from two opposing contributions: at longer wavelengths, the diminished FR of Ce:YIG is balanced by a wavelength squared dependence of nonreciprocal resonance shift (refer to the Supporting Information for details of the simulations and further discussions).

Based on the isolator characterization outcome, we quantified the different factors contributing to the overall loss in the device. Total loss in the nonreciprocal resonator is computed from the measured loaded Q-factor and extinction ratio to be 17.5 dB/cm, representing over 70% reduction compared to our prior report. Propagation loss of GeSbSe racetrack resonators without the underlying magneto-optical oxide layers but with an otherwise identical configuration was
experimentally assessed to be 3.8 dB/cm, among which 1.1 dB/cm is attributed to scattering losses at the four straight-to-bend junctions in the racetrack resonator according to our finite-difference time-domain (FDTD) simulations. Such scattering losses can be easily eliminated by replacing the circular segments in the racetrack with a curve that has a continuous curvature change along its length, such as Euler spiral or Bezier curve. \(^5\) \(^3\) \(^5\) The remaining 2.7 dB/cm, presumably resulting from sidewall roughness scattering in the GeSbSe waveguide, is higher than the 0.5 dB/cm loss value we previously measured in ChG microring resonators, \(^4\) \(^4\) indicating considerable room for further performance improvement. Loss caused by optical absorption in the magneto-optical films is therefore 13.7 dB/cm, corresponding to 270 dB/cm absorption in the magneto-optical oxides. This figure is considerably higher than loss we previously measured in polycrystalline Ce:YIG films (40 dB/cm at 1550 nm).\(^3\) We thus anticipate that the IL of monolithic on-chip isolators can be further minimized with optimized garnet deposition protocols to bring their performance to a level on par with or even superior to their bulk counterparts.

It worth pointing out that the narrow working bandwidth of the resonator-based isolator precludes their application with broadband tunable lasers. When used in conjunction with single-wavelength lasers (which represents most use cases in PICs), the device’s isolation wavelength can be tuned to match the laser emitting wavelength by thermo-optic locking or photosensitive trimming, an added benefit of using ChGs as the waveguide material.\(^5\) \(^5\) In addition, the strip-loaded magneto-optical waveguide design coupled with low-loss vertical tapers is equally applicable to reducing loss in broadband interferometer isolators, and therefore, our approach will likely have a broad impact on on-chip isolator integration.

### CONCLUSION

In this article, we have significantly advanced the state-of-the-art in monolithic integrated optical isolators by demonstrating an on-chip isolator with a record low insertion loss of 3.0 dB and an isolation ratio of 40 dB. The exceptional isolation performance stems from a novel device architecture combining a strip-loaded waveguide configuration with an adiabatic vertical taper, which effectively eliminates parasitic optical losses due to secondary phase absorption and junction scattering while, at the same time, boosting waveguide nonreciprocity. The high-performance, all-passive on-chip isolator constitutes a critical step toward planar integration of optical isolation devices with large-scale photonic circuits.

### METHODS

**MO Material Deposition.** Si wafers coated with 3 μm wet oxide (MTI Corp.) were used as the substrate. The wafers were first cleaned in a piranha bath to remove contaminants. A 50 nm unsubstituted YIG layer was then deposited onto the substrate using pulsed laser deposition (PLD) at a substrate temperature of 400 °C and an oxygen pressure of 5 mTorr. The as-deposited YIG film is amorphous, and the film was subsequently rapid thermal annealed at 850 °C for 2 min to form a polycrystalline seed layer. A 90 nm Ce:YIG film was deposited onto the seed layer at a substrate temperature of 750 °C and 10 mTorr O₂ pressure using PLD. Composition of the film was quantified using inductively coupled plasma mass spectrometry (ICP-MS) to be Ce₂Y₃Fe₅O₁₂. The Ce:YIG film formed at the temperature spontaneously crystallizes during growth facilitated by the unsubstituted YIG seed layer.
Device Fabrication. A 500 nm silicon oxide spacer layer was deposited on top of Ce:YIG using plasma-enhanced chemical vapor deposition (PECVD) with a gas mixture of silane and N₂O at 500 mTorr pressure and a plasma power of 80 W. The oxide vertical taper structure was subsequently defined using grayscale lithography. The continuous-tone grayscale photomask was fabricated via electron beam writing in an off-the-shelf High Energy Beam Sensitive (HEBS) glass plate (Canyon Materials, Inc.). The writing dose was calibrated following standard calibration procedures detailed elsewhere.⁵⁶ During the grayscale lithography process, an AZ4110 photoresist (MicroChemicals GmbH) layer with 1 μm thickness was spin coated onto the substrate and baked at 100 °C for 1 min. The resist was exposed on a Karl Suss MA-4 mask aligner at a total dose of 100 mJ/cm² and then developed in AZ400K plate (Canyon Materials, Inc.). The writing dose was calibrated in an o-

Notes

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Supporting Information

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